REMARKS

Claims 1-20 are currently pending in the application. Of these claims, claims 1 and 10 are independent.

Erroneous Reference Citation

Applicant notes the following reference citation on the Notice of References Cited (PTO-892) Form attached as part of the Office Action:

> Burleson et al.; Bus-Invert Coding for Low-Power I/O; IEEE Transactions on Very Large Scale Integration (VLSI) Systems; Vol. 5, No. 4; Dec. 1997; Pages 49-58.

is in error and should read:

> Burleson et al.; Bus-Invert Coding for Low-Power I/O; IEEE Transactions on Very Large Scale Integration (VLSI) Systems; Vol. 3, No. 1; Mar. 1995; Pages 49-58.

Applicant respectfully requests correction of this citation, perhaps by crossing out the erroneous citation and entering the correct citation on a new PTO-892 Form as part of the next Office Action, so the correct citation is printed on the cover of any patent that may be granted from this application.

Claim Rejections

Claims 1-5 and 10 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the BACKGROUND section of the present application ("Background") in view of *Low-Power Encodings for Global Communication in CMOS VLSI* by Stan and Burleson ("Burleson").

Claims 6-9 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Background in view of Burleson and further in view of U.S. Patent No. 4,453,229 to Schaire ("Schaire").

Applicant respectfully traverses these rejections as follows.

Independent claim 1 recites generation of an encoded signal in response to a transition at an input between a current clock cycle and a previous clock cycle.

Independent claim 10 recites generation of an encoded signal in response to a transition at an input node between a current clock cycle and a previous clock cycle.

Applicant respectfully submits none of Background, Burleson, or Schaire, whether alone or in any combination with one another, taught or suggested such feature(s) as claimed.

The Office Action equates such feature(s) with reference to the transition signaling taught on page 445 of Burleson and, more specifically, with reference to the modulation equation in column 2 on page 445 of Burleson.

Burleson, however, taught on page 445 in column 1 at lines 18-20:

> With transition signaling a logical 1 is represented by a *transition* (from HI to LO or LO to HI) while a 0 is represented by the lack of such a transition.

Burleson therefore taught generation of an encoded signal (i.e., a transition or no transition) in response to a level of an input signal (i.e., a logical 1 or a logical 0). Applicant respectfully submits this teaching cannot be equated with generation of an encoded signal in response to a transition as claimed.

Burleson also taught on page 445 in column 2 at lines 2-6:

> Denoting by $v(t)$ the symbol to be transmitted, the modulated symbol with transition signaling, $b(t)$, is obtained by the simple expression * * *
>
> $$b(t) = v(t) \text{ [bitwise XOR] } b(t - 1).$$

Here, Burleson again taught generation of an encoded signal (i.e., a transition between $b(t - 1)$ and $b(t)$ or no transition between $b(t - 1)$ and $b(t)$) in response to a level of an input signal (i.e., $v(t)$). Applicant respectfully submits this teaching cannot be equated with generation of an encoded signal in response to a transition as claimed.

Noting the remaining rejected claims depend from independent claim 1, Applicant therefore respectfully submits the rejections of claims 1-10 have been overcome and should accordingly be withdrawn.

New Claim

New claim 20 depends indirectly from independent claim 10. Applicant therefore respectfully submits new claim 20 is patentable.

Note that there may be additional reasons for the patentability of claims. For example, there may be additional reasons why the dependent claims are patentable.

It is respectfully submitted this patent application is in condition for allowance, for which early action is earnestly solicited.

The Examiner is invited to telephone the undersigned to help expedite the prosecution of this patent application.

Respectfully submitted,

Date: November 28, 2005

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